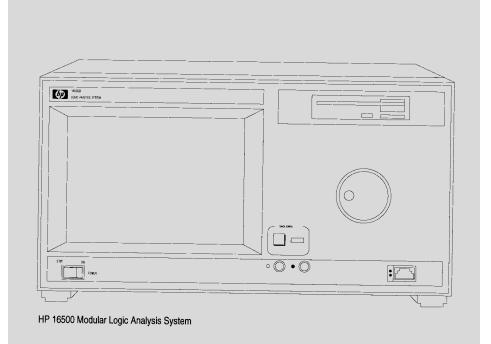
HP 16500 Modular Logic Analysis System

- Up to 340 Channels of 100 MHz State / 400 MHz Timing Analysis at 1-Meg Deep
- Up to 80 Channels, of 4 GHz Timing Analysis
- 2-GSa/s Digitizing Oscilloscope (500 MHz Analog Bandwidth)
- 200-Million-Vector-Per-Second Pattern Generator
- Debug Your System Using Symbolics
- Source Code Referenced to (IA) Microprocessor Bus Trace
- Easy Network Access

Ordering Information:

Hewlett Packard's 16500 Modular Logic Analysis System captures real-time interactions between Embedded Intel Architecture (IA) microprocessor system hardware and software. A variety of measurement modules provide a complete overview of your system's functionality and performance. The HP 16500 system measurement modules help debug problems and determine the root cause of (IA)-based system defects. The system has been tuned for software debugging and has a user interface that helps users learn the system quickly and easily.



The HP 16500B logic analysis system consists of a mainframe that supports up to 5 independent measurement modules. An optional expansion frame supports an additional 5 modules.

PROCESSORS SUPPORTED:

Refer to Logic Analyzer and Preprocessor Interfaces page in this section.

AVAILABILITY:

Now

CONTACT:

HP Microprocessor Development Hotline Phone: (800) 447-3282 FAX: (719) 590-5054

Hewlett-Packard Company Attn: Sales Development Dept. P.O. Box 617 Colorado Springs, CO 80901-0617 Phone: (719) 590-5801 For international contacts see Appendix B.

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